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M844	08-13-2001	12	<input checked="" type="checkbox"/>	08-10-2005 23:25:56 IDS CONV

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Day : Wednesday
Date: 8/31/2005
Time: 16:30:27

Inventor Information for 09/929714

Inventor Name	City	State/Country
FALKENBERG, ANDREAS	ESCONDIDO	CALIFORNIA

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Inventor Name Search Result

Your Search was:

Last Name = FALKENBERG

First Name = ANDREAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09830623	Not Issued	41	09/06/2001	Rake receiver in third generation mobile radiotelephone systems	FALKENBERG, ANDREAS
09924620	Not Issued	41	08/07/2001	System and method for rate adaptation in a wireless communication system	FALKENBERG, ANDREAS
09929714	Not Issued	80	08/13/2001	Method of and system for modulating and demodulating a communication signal using differential quadrature phase shift keying (DQPSK)	FALKENBERG, ANDREAS
09957204	Not Issued	61	09/20/2001	System for and method of protecting data in firmware modules of embedded systems	FALKENBERG, ANDREAS
09959231	6690312	150	12/11/2001	METHOD AND CIRCUIT FOR REGULATING THE SIGNAL LEVEL FED TO AN ANALOG/DIGITAL CONVERTER	FALKENBERG, ANDREAS
09959258	Not Issued	92	10/22/2001	METHOD AND DEVICE FOR DECODING A CODE MULTIPLEX SIGNAL	FALKENBERG, ANDREAS
10036246	6526428	150	10/22/2001	METHOD AND APPARATUS FOR DETERMINING INTERPOLATED INTERMEDIATE VALUES OF A SAMPLED SIGNAL	FALKENBERG, ANDREAS
10381014	Not Issued	30	08/18/2003	Method for calibrating the frequency of an rf oscillator in a mobile part of a mobile communications device	FALKENBERG, ANDREAS
10381149	Not Issued	30	08/19/2003	Method for Frequency Acquisition of a Mobile Communications Device	FALKENBERG, ANDREAS
10465292	Not Issued	30	06/18/2003	Retargetable compiler using intermediate code with explicit operands	FALKENBERG, ANDREAS
10906702	Not Issued	20	03/02/2005	HIGH-LEVEL LANGUAGE PROCESSOR APPARATUS AND METHOD	FALKENBERG, ANDREAS
60274542	Not Issued	159	03/08/2001	Rate-adaptation (jittering) design	FALKENBERG, ANDREAS
60390682	Not	159	06/18/2002	Intermediate format for use with a	FALKENBERG,

Issued	compiler/analyser/RTL model generator system and simulator and unified format to describe hardware and software components in libraries and platforms for synthesis and compiler systems and using flexible translate methods in the code generation section of retargetable compilers	ANDREAS
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Inventor Search Completed: No Records to Display.

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